

# CURRICULUM VITAE

## Hiromichi Shinohara



**Birth date**    March 15, 1954

**Academic background:**

1978 Master of Science and Engineering, Waseda University

**Professional career:**

April	1978	Joined Nippon Telegraph and Telephone Public Corporation
June	2009	Senior Vice President Head of Research and Development Planning Member of the Board of the Company
June	2011	Senior Vice President Head of Research and Development Planning Head of the Information Sharing Laboratory Group Member of the Board of the Company
Oct.	2011	Senior Vice President Head of Research and Development Planning Member of the Board of the Company
June	2012	Executive Vice President Head of Research and Development Planning Member of the Board of the Company
June	2014	Senior Executive Vice President Head of Research and Development Planning Representative Member of the Board of the Company
June	2018	Chairman of the Board of the Company

**Present**

Chairman of the Board of Nippon Telegraph and Telephone Corporation  
Vice Chairs and Chair of the Committee on Digital Economy,  
the Japan Business Federation (Keidanren )  
Executive Member of the Council for Science, Technology and Innovation

As of May 6th, 2019